

Atty. Dkt. No. SOEI/0016

1765

In re Application of:  
Tamitani, et al.

Serial No.: 10/030,525

Confirmation No.: 5906

Filed: July 24, 2002

For: Semiconductor Device And  
Method Of Manufacturing A  
Semiconductor Device

**Group Art Unit: 1765**

Examiner: William A. Powell


**MAIL STOP NON-FEE AMENDMENT**  
**Commissioner for Patents**  
**P.O. Box 1450**  
**Alexandria, VA 22313-1450**

Dear Sir:

CERTIFICATE OF MAILING  
37 CFR 1.8

I hereby certify that this correspondence is being deposited on  
February 27, 2004, with the United States Postal Service as  
First Class Mail in an envelope addressed to: Commissioner  
for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 27, 2004  
Date

  
Signature

**RESPONSE TO RESTRICTION REQUIREMENT DATED FEBRUARY 3, 2004**

In response to the Restriction Requirement dated February 3, 2004, having a shortened statutory period for response set to expire on March 3, 2004, please enter this response and reconsider the claims pending in the application for reasons discussed below. Although Applicant believes that no additional fees are due in connection with this response, the Commissioner is hereby authorized to charge counsel's Deposit Account No. 20-0782/SOEI/0016/KMT, for any fees, including extension of time fees or excess claim fees, required to make this response timely and acceptable to the Office.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper. **Remarks** begin on page 5 of this paper.